

## Overview:

- Plan 9 is an IC Design Services company specializing in *Analog, Mixed Signal, and RF* designs.
- Plan 9 has provided IC and engineering solutions for over 23 years.
- Plan 9 strives to establish and maintain mutual expectations with the customer and then works to fulfill those goals.

## Who are the principals:

### *Donald Whitney*

- Over 37 years of experience
- Listed as inventor on 13 issued patents, 8 pending.
- BSEE and MSEE
- IEEE Senior Member

## Proprietary IP offering:

- Plan 9 developed three products to address the narrow-band Power Line Communications (PLC) market.
- These P1000 products are a low distortion, 3Ap output Line driver, a two PGA receiver, and a reference/ thermal shutdown.
- The designs are silicon tested with eval kits available.
- Developed P2000, an improved P1000, capable of directly driving a 2nF load.
- Plan 9 is currently offering this IP for licensing or sale.

## Recent design work:

### *Research and Development of wearable IoT medical devices:*

- Human compatible, researched anatomy to determine how the circuitry would function with the body and meet FDA safety requirements.
- Very small footprint, flip-chip, and designed flex PCB board for chipset.
- new to the world medical application

### *Designed integrated circuits for wearable devices:*

- Ultra-low power circuitry with power savings system implemented.
- Ultra-low off current leakage
- All subthreshold design
- Circuits include high voltage (50V) charge pump, regulator, oscillators, references, state machines, interface, ESD, etc.

## Examples of other design projects:

- RFID 13.56MHz circuitry
- Emergency radio baseband transceiver
- Time of Flight precision laser driver
- DC/DC Boost power supply
- 70MHz power line modem AFE for HomePlug applications
- Interface circuits, HV RS-485, DDR
- Delta Sigma ADC/DAC, other data converters

## Examples of processes used by Plan 9:

- Global Foundries 55nm, 65nm, 130nm BCD Lite HV CMOS (24V)
- IBM/GF/TSI 7HV CMOS 0.18um 50V LDMOS
- IBM 6RF (0.25um), 8RF (130nm), 9LP (90nm)
- TSMC CMOS 0.15um, 0.35um
- Ams SiGe S35 0.35um 12V, C35
- Various other BiCMOS, BJT, DI and HV processes

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